

#0803

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	EXTERNAL VERIFICATION OF PACKAGE PROCESSED LINEWIDTHS AND SPACINGS IN SEMICONDUCTOR PACKAGES						
Application Number :	10 / 604933						
Confirmation Number:							
First Named Applicant:	Thomas Fleischman						
Attorney Docket Number:	FIS920030167US1						
Art Unit:	2829						
Examiner:	Karlson						
Search string:	( 5276963 ).pn						
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
E.T.K.	1	5276963	1994-01-11	FLANDERS			
<b>Signature</b>							
Examiner Name				Date			
Ernest T. Karlson				September 1, 2004			